

EUROPEAN PATENT OFFICE

Patent Abstracts of Japan

PUBLICATION NUMBER : 2000150522
 PUBLICATION DATE : 30-05-00

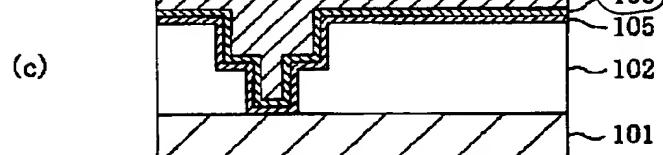
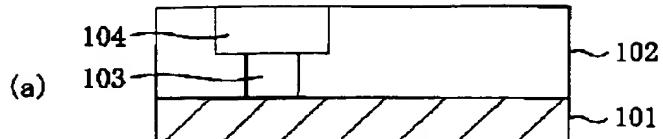
APPLICATION DATE : 12-01-98
 APPLICATION NUMBER : 2000006049

APPLICANT : MATSUSHITA ELECTRONICS
 INDUSTRY CORP;

INVENTOR : SEKIGUCHI MITSURU;

INT.CL. : H01L 21/3205 H01L 21/768

TITLE : MANUFACTURE OF
 SEMICONDUCTOR DEVICE



ABSTRACT : PROBLEM TO BE SOLVED: To provide a reliable semiconductor device that has buried wiring with superior electromigration resistance.

SOLUTION: A contact hole 103 and a recessed groove 104 for wiring are formed on an interlayer insulation film 102 being deposited on a semiconductor substrate 101, and a TiN/Ti film 105 being used as a diffusion prevention film is formed on the wall surfaces of the contact hole 103 and the recessed groove 104. A copper alloy film 106 made of Cu-Sn alloy, Cu-Mg alloy, or Cu-Zr alloy is deposited on the TiN/Ti film 105 by the sputtering method, and a copper film 107 is deposited on the copper alloy film 106 by the CVD method or the plating method. By heat treatment, Sn, Mg, or Zr contained in the copper alloy film 106 is diffused into a copper film 107 for forming the copper alloy film where the Sn, Mg, or Zr is contained in Cu, the copper alloy film is subjected to the CMP method, and contact consisting of the copper alloy film where the Sn, Mg, or Zr is contained in the Cu and buried wiring are simultaneously formed.

COPYRIGHT: (C)2000,JPO